

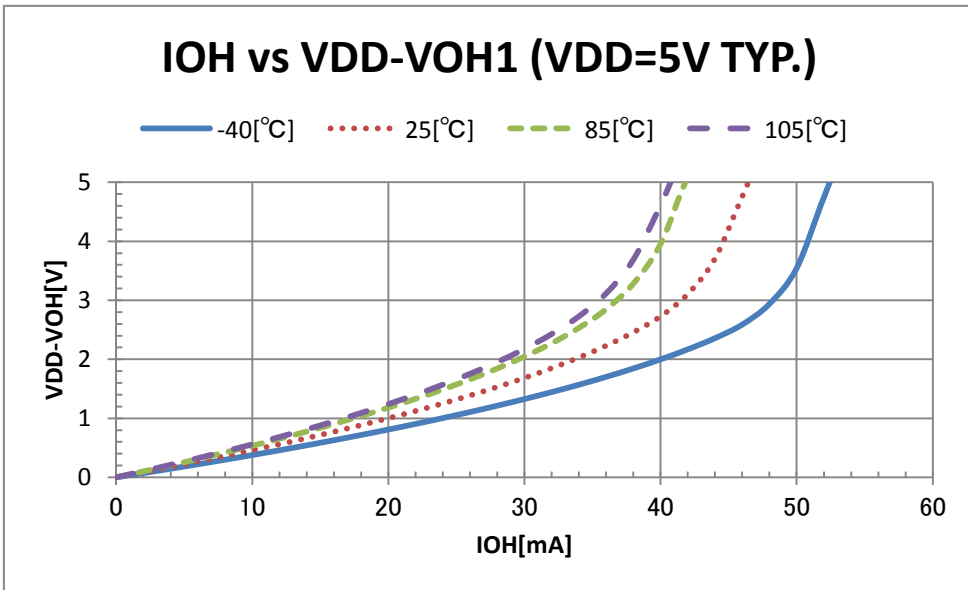
ML62Q1200 1400 1600 特性グラフ

これらのグラフは、アプリケーション設計のための参考値です。

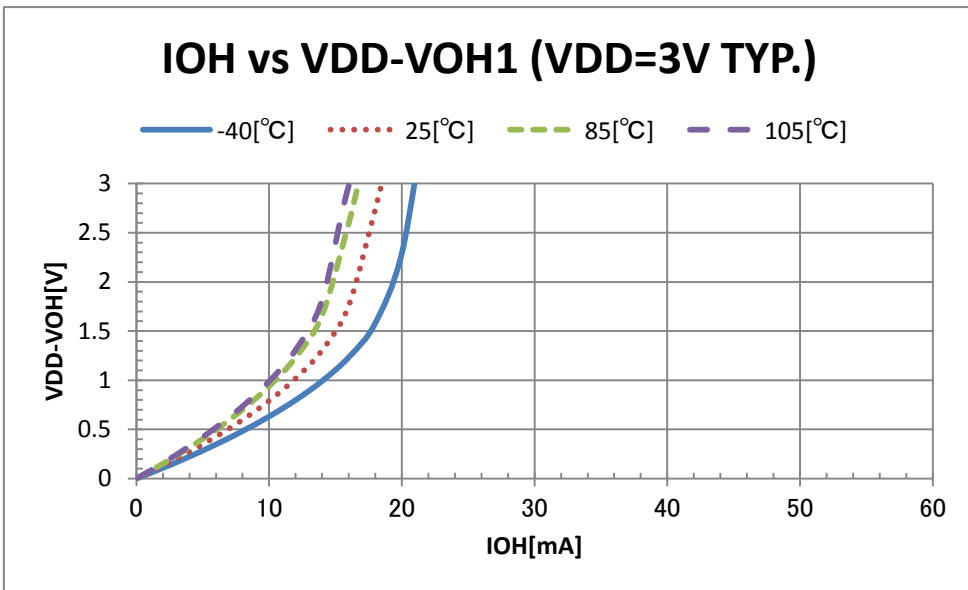
These Graphs are reference for designing an application.

IOH VS VDD-VOH1

VDD=5V (TYP.)

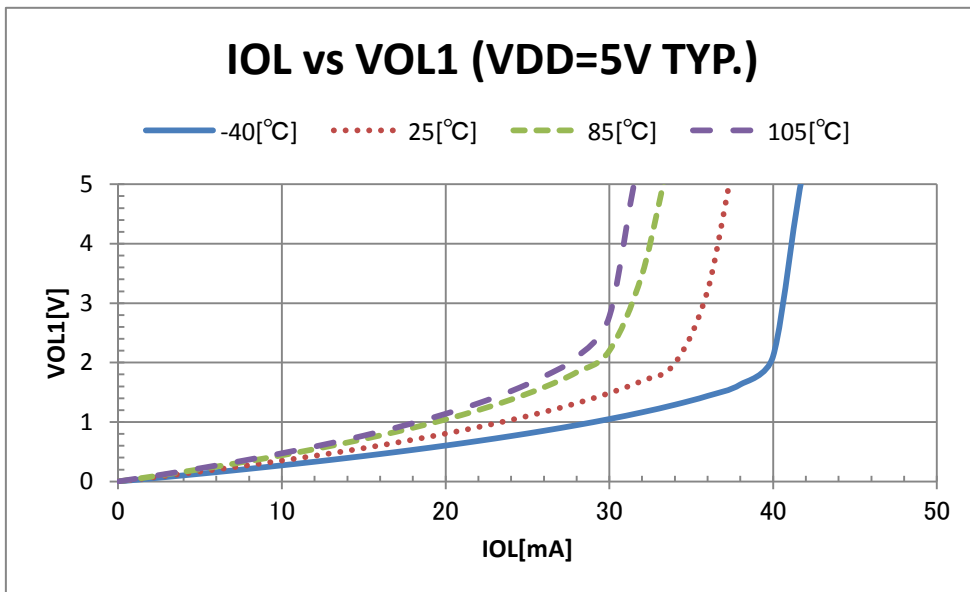


VDD=3V (TYP.)

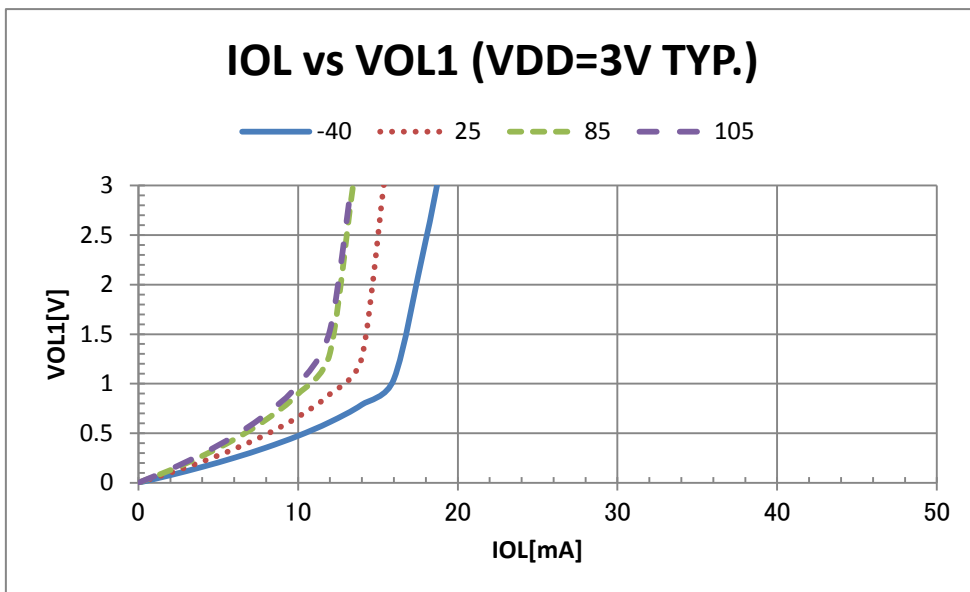


IOL VS VOL1

VDD=5V (TYP.)

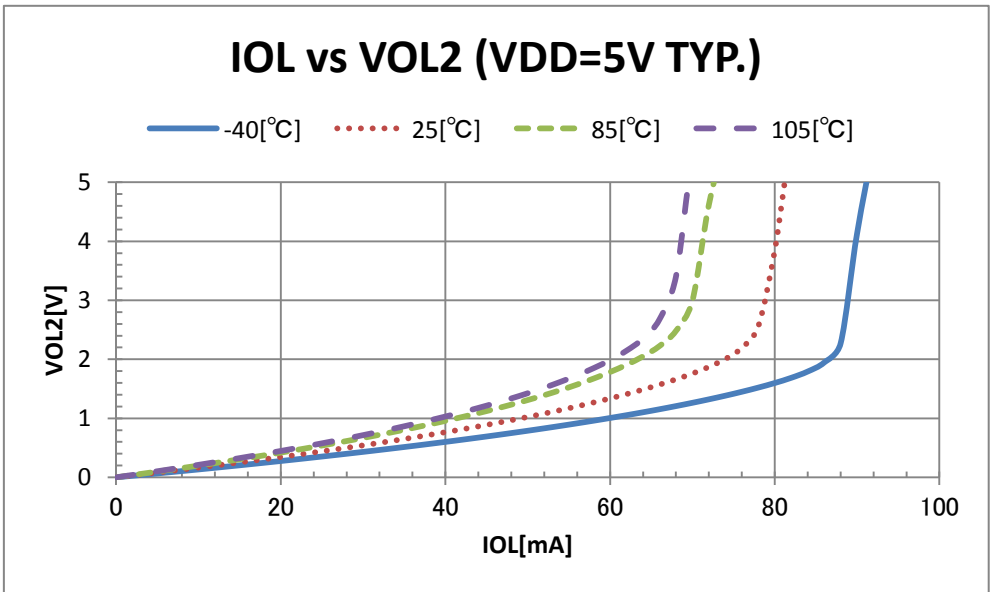


VDD=3V (TYP.)

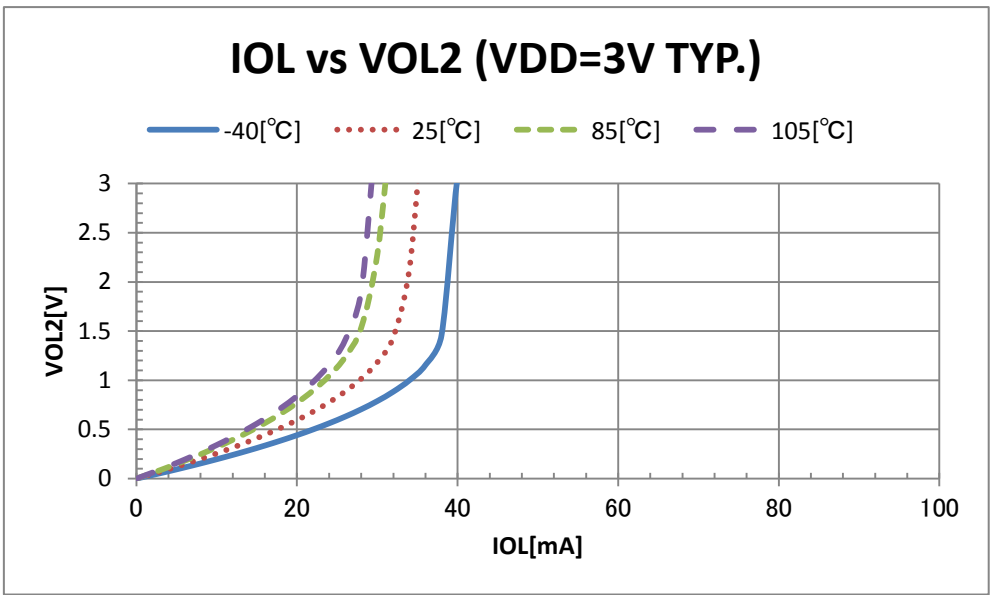


IOL VS VOL2

VDD=5V (TYP.)

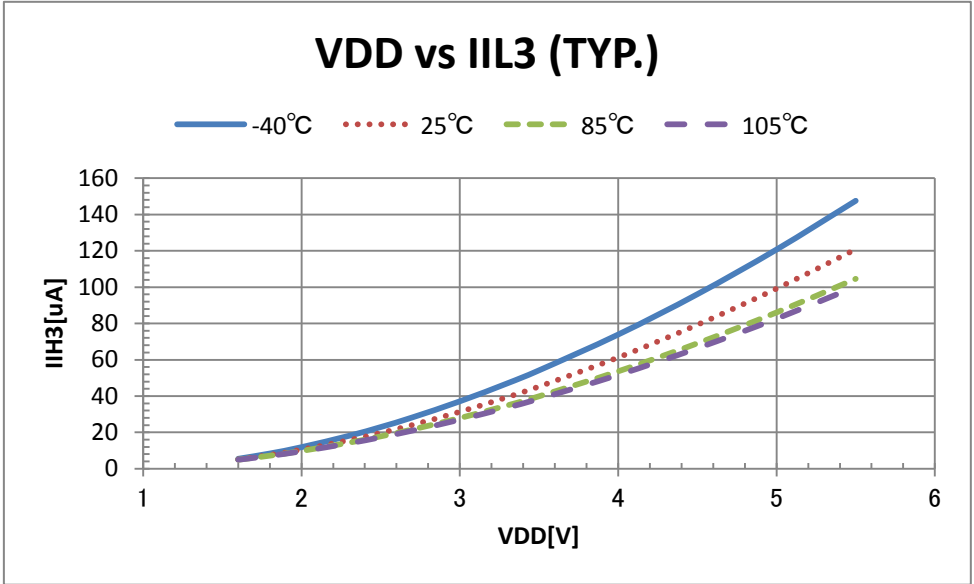


VDD=3V (TYP.)

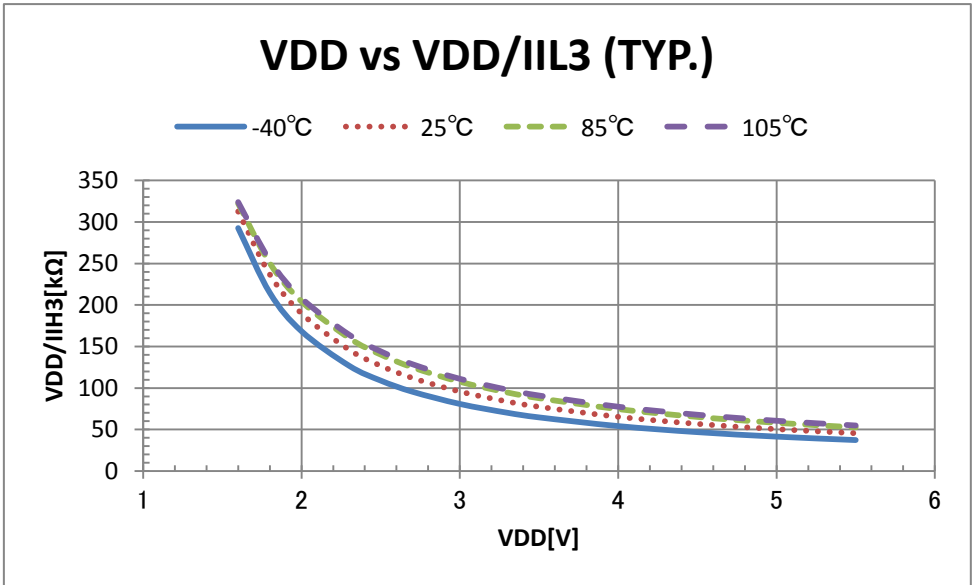


Pull-up resistor

VDD VS VDD/IIL3 (TYP.)



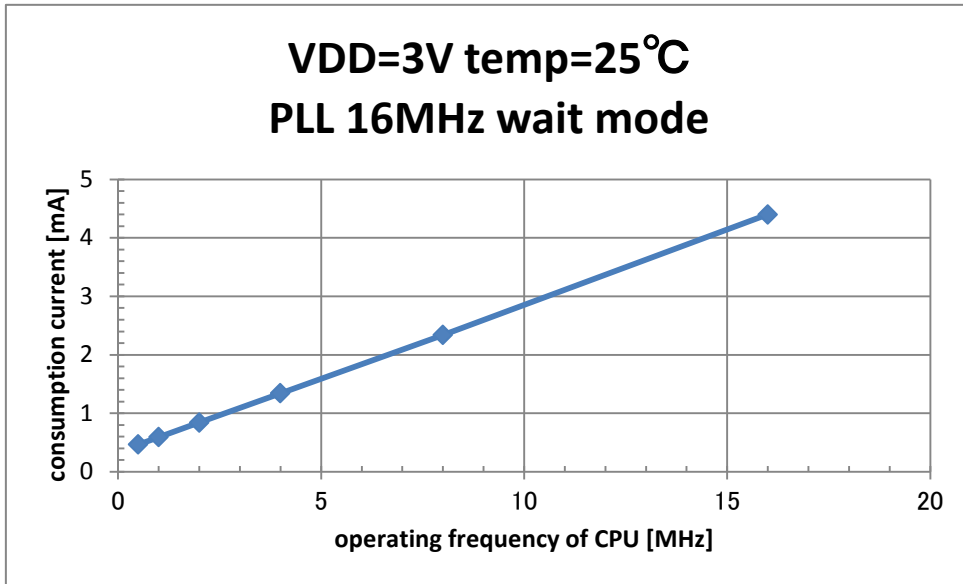
VDD VS VDD/IIL3 (TYP.)



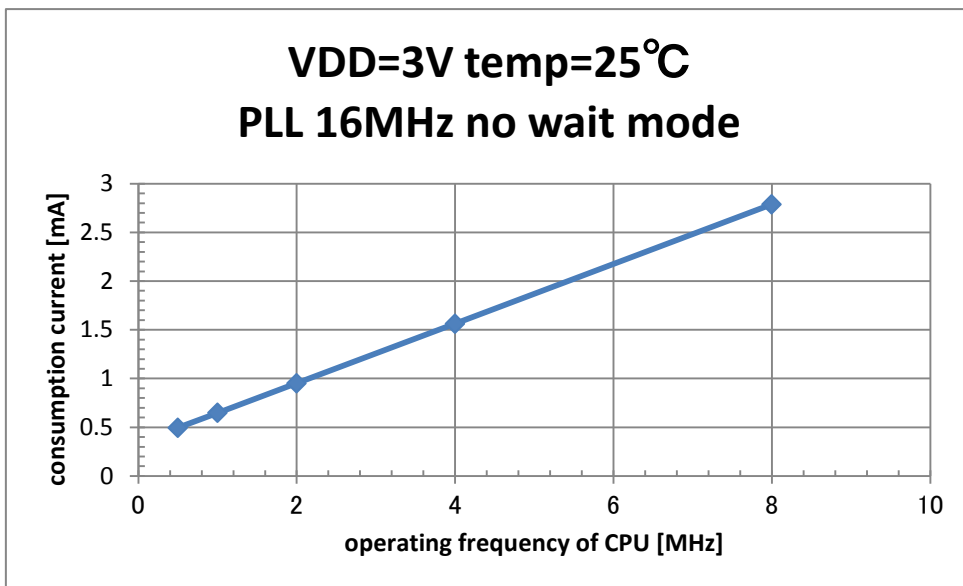
Current consumption VS operating frequency of CPU

VDD=3V, temp=25°C, stop the clock supply to peripherals.

PLL 16MHz mode, CPU Wait mode (TYP.)

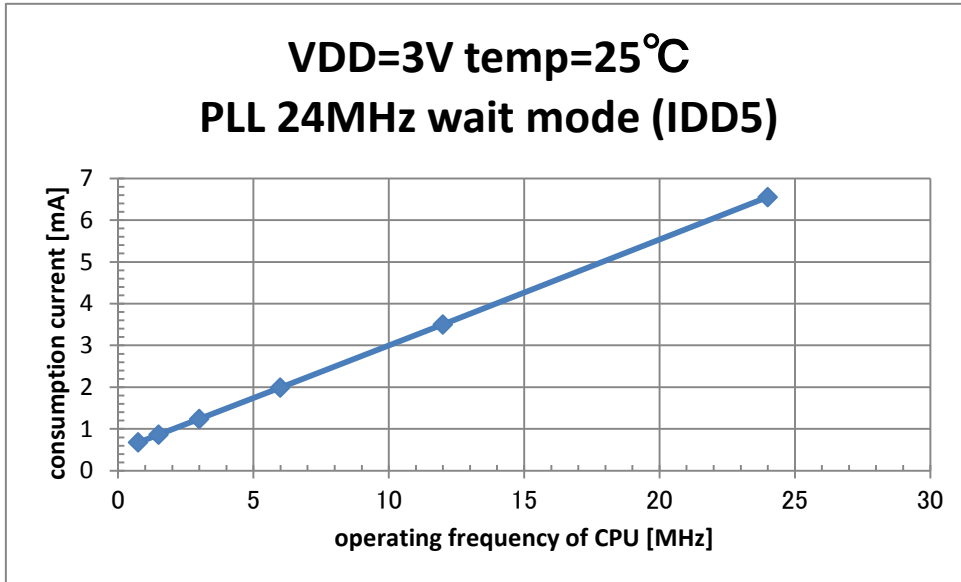


PLL 16MHz mode, CPU no Wait mode (TYP.)

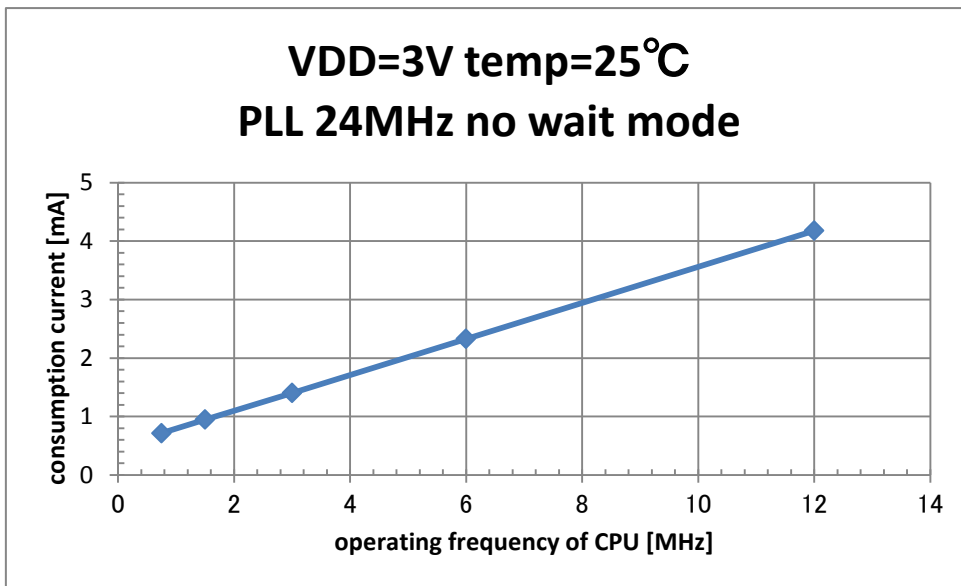


VDD=3V, temp=25°C, stop the clock supply to peripherals.

PLL 24MHz mode, CPU Wait mode (TYP.) (IDD5)

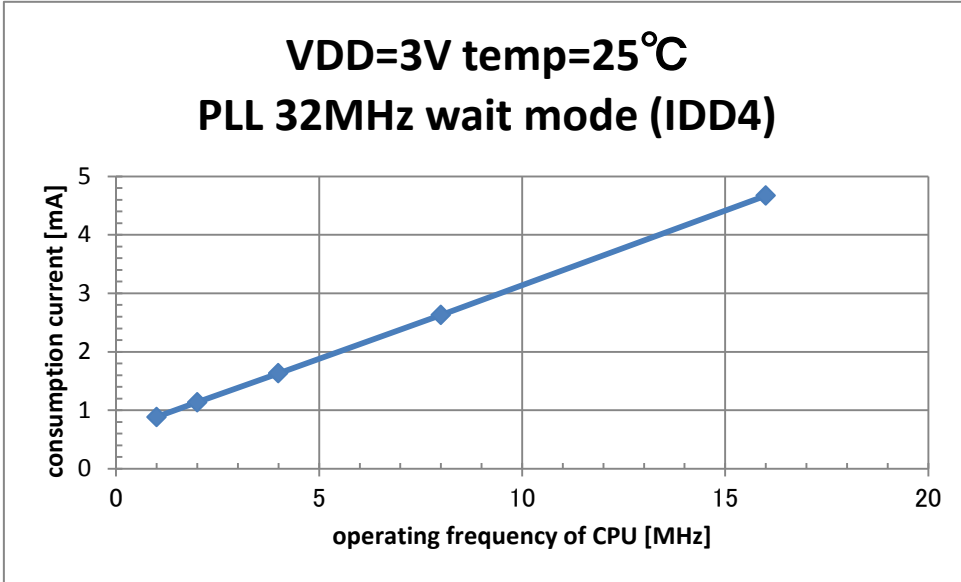


PLL 24MHz mode, CPU no Wait mode (TYP.)

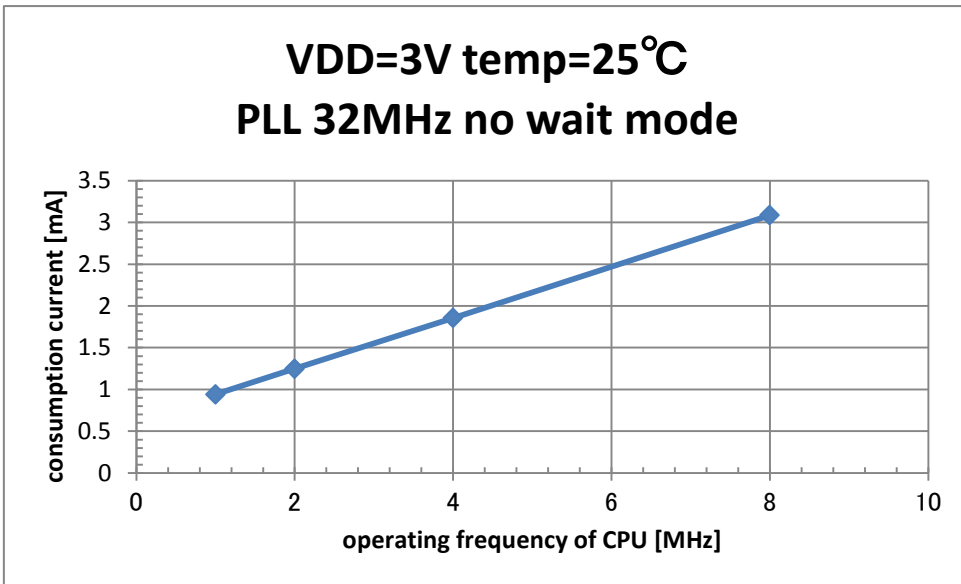


VDD=3V, temp=25°C, stop the clock supply to peripherals.

PLL 32MHz mode, CPU Wait mode (TYP) (IDD4)

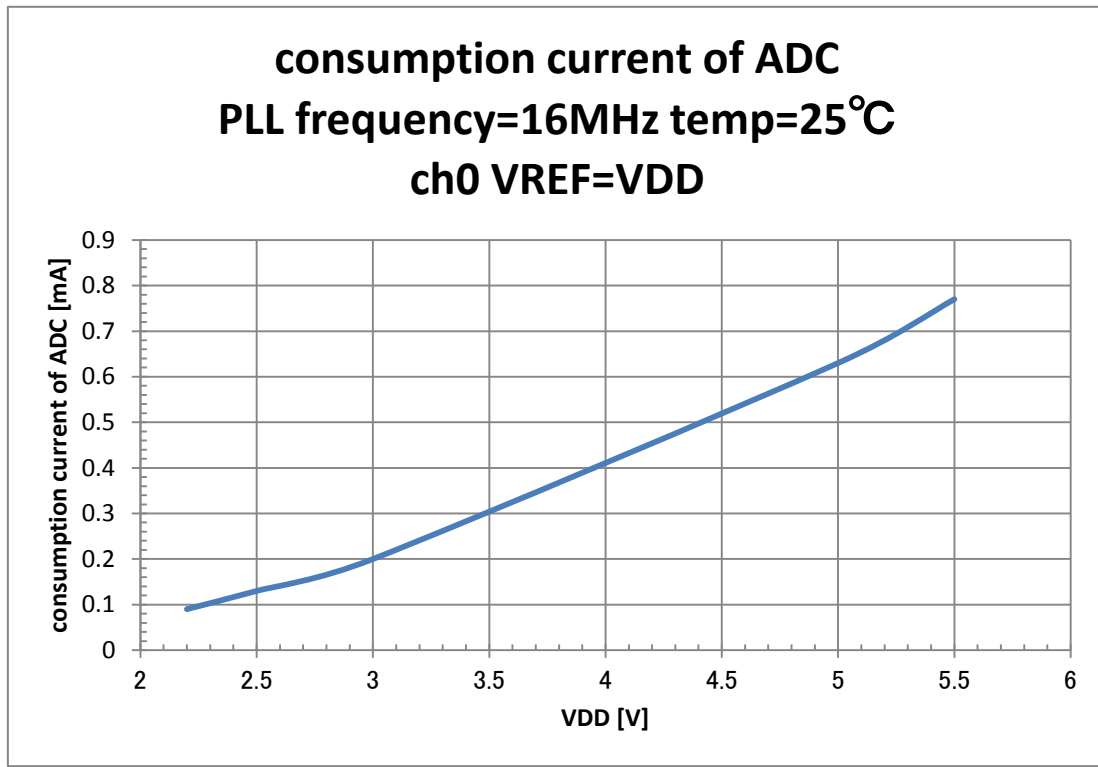


PLL 32MHz mode, CPU no Wait mode (TYP)



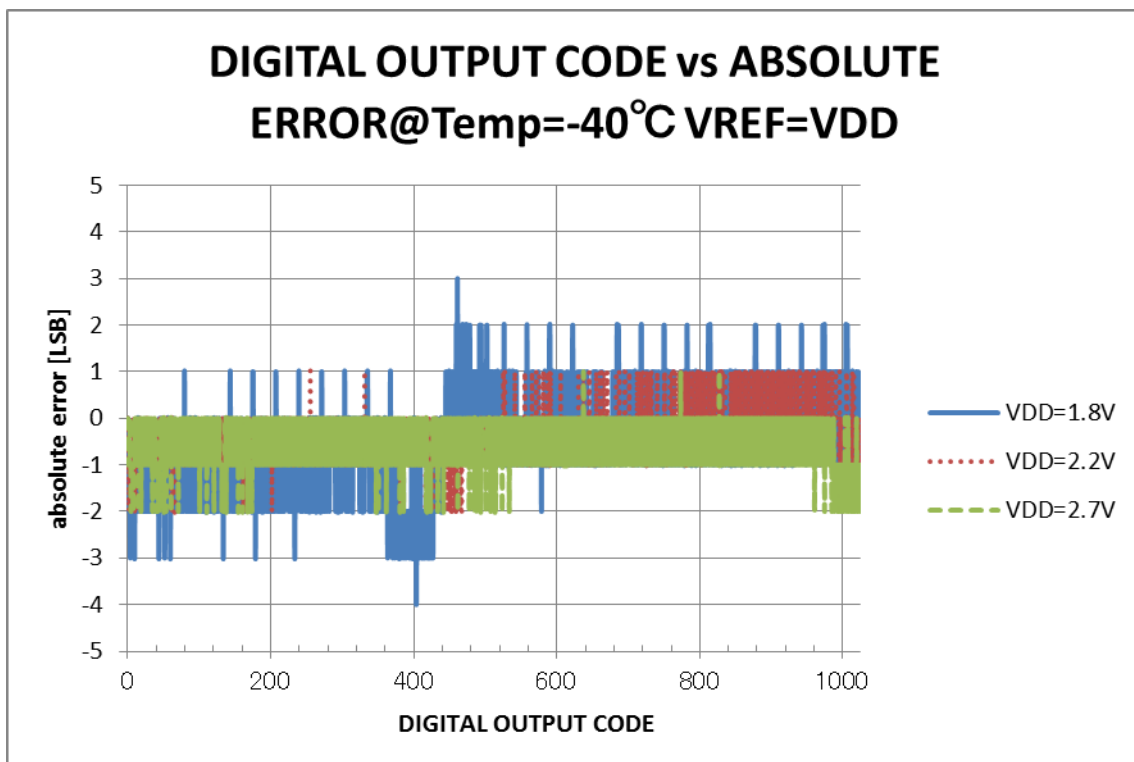
Consumption current of ADC VS operating voltage

temp=25°C (TYP)

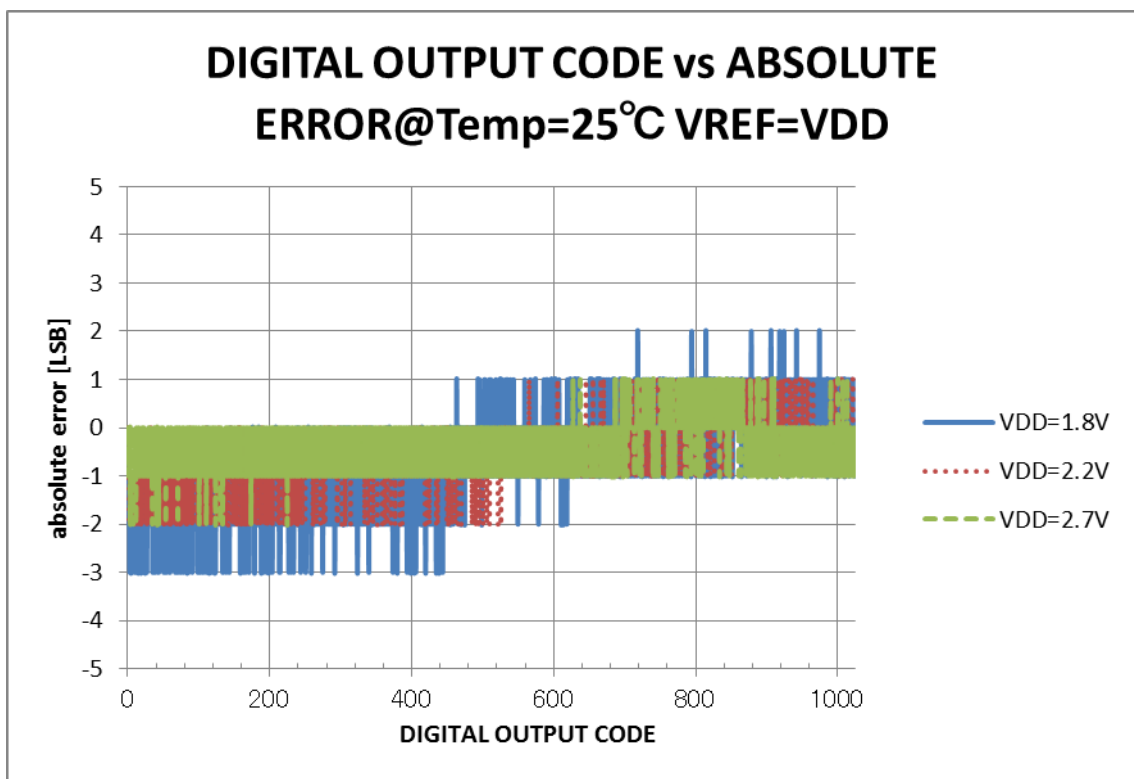


DIGITAL OUTPUT CODE vs absolute error of ADC

Temp=-40°C (TYP.)



Temp=25°C (TYP.)



Temp=105°C (TYP.)

